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SN # 09/550,477

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PATENT & TRADEMARK OFFICE

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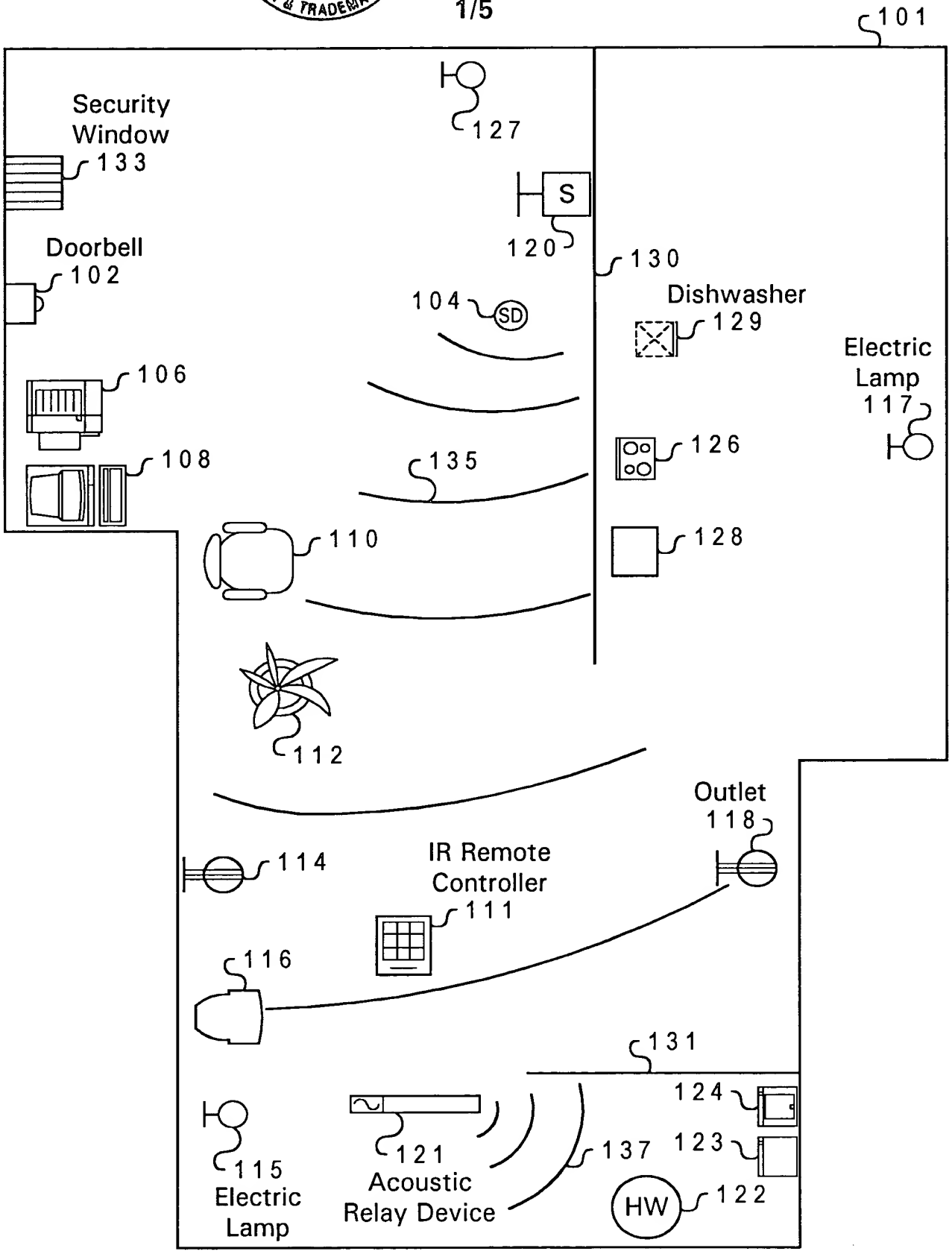

Fig. 1

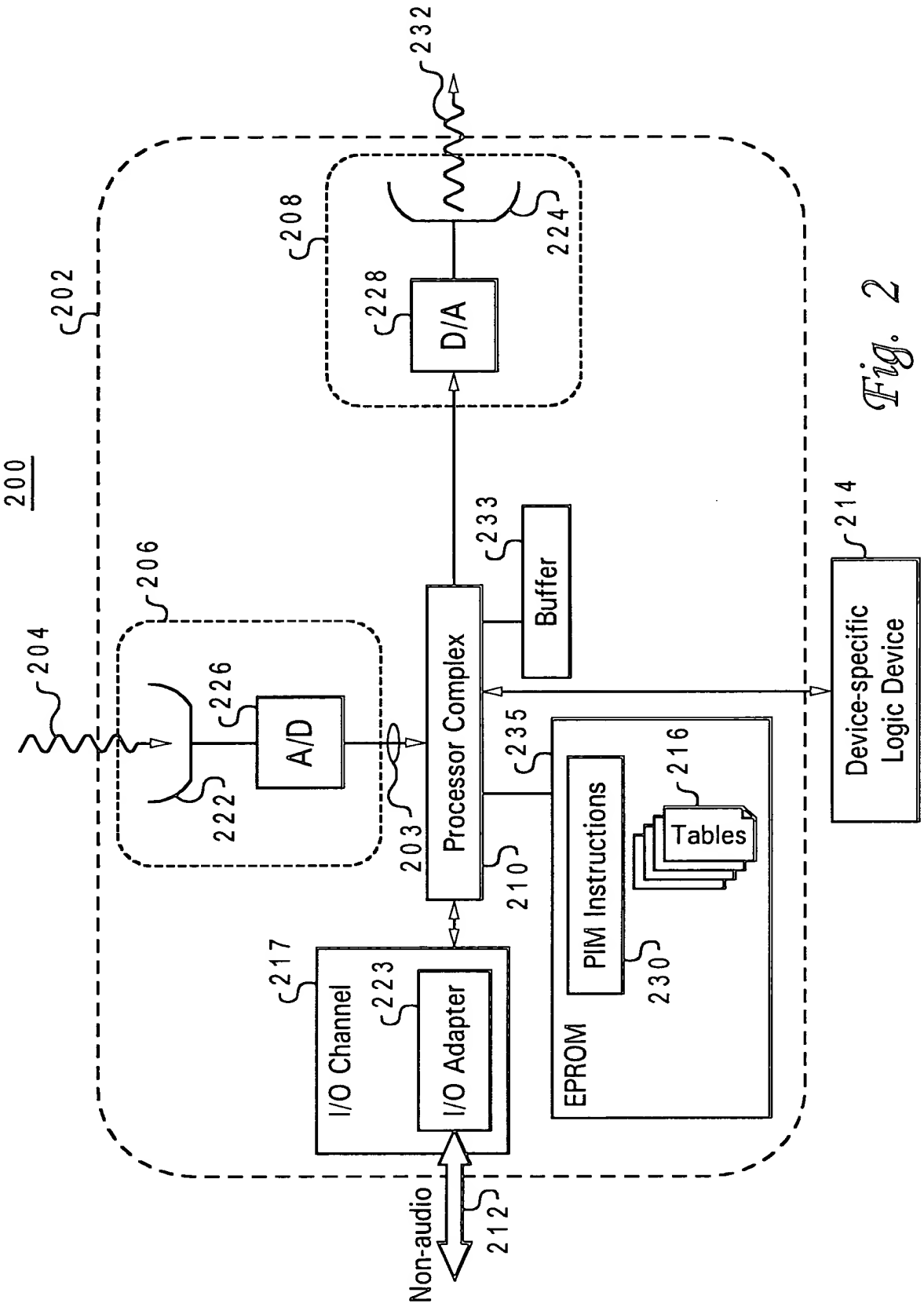
200
202
204
206
208
222
224
226
228
232
233
214
A/D
D/A
Buffer
Processor Complex
Device-specific Logic Device
203
210
235
216
Tables
PIM Instructions
230
EPROM
217
223
I/O Channel
I/O Adapter
212
Non-audio

Fig. 2

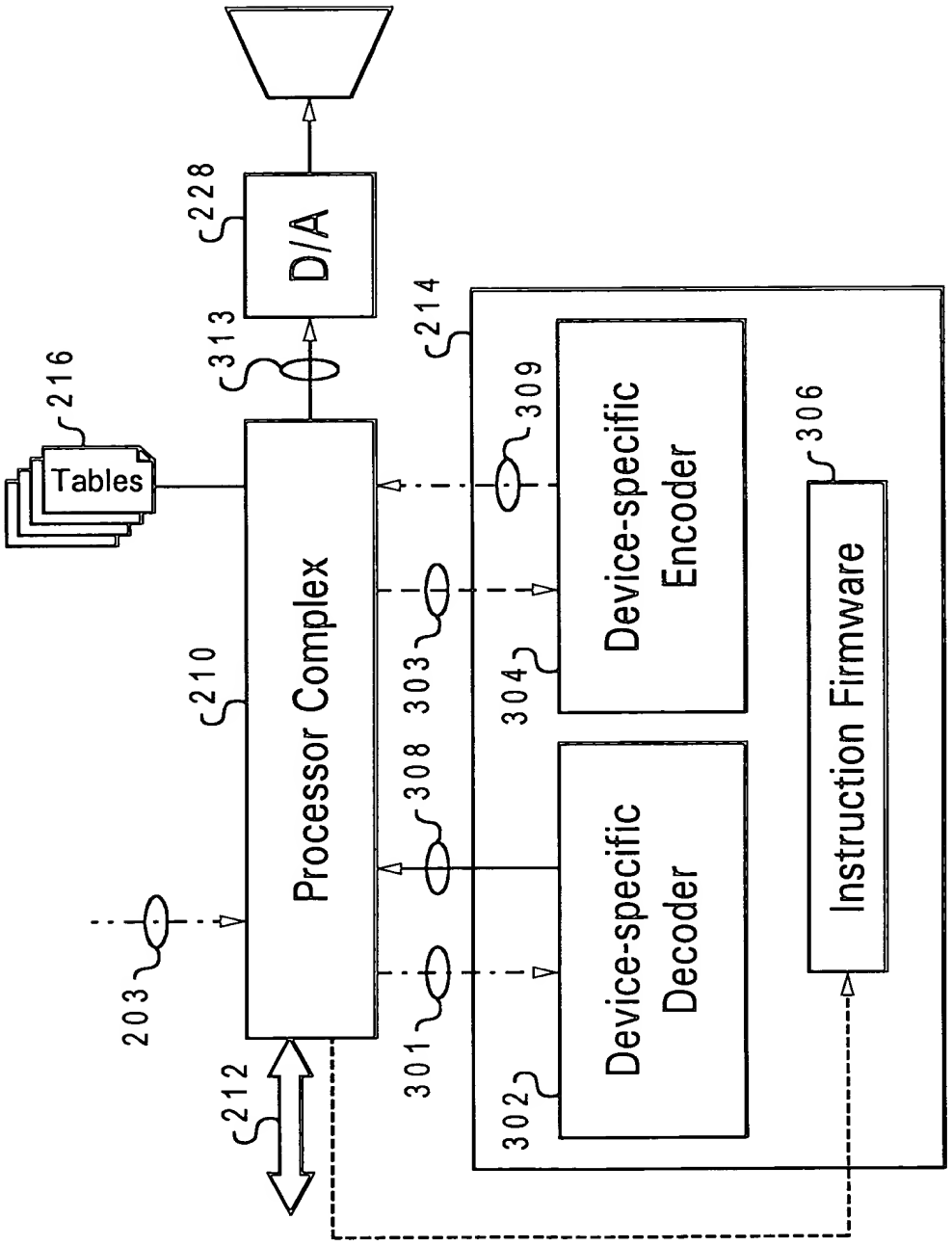
D/A
228
313
214
216
Tables
309
Device-specific Encoder
306
Instruction Firmware
210
Processor Complex
303
304
308
203
301
302
Device-specific Decoder
212

Fig. 3

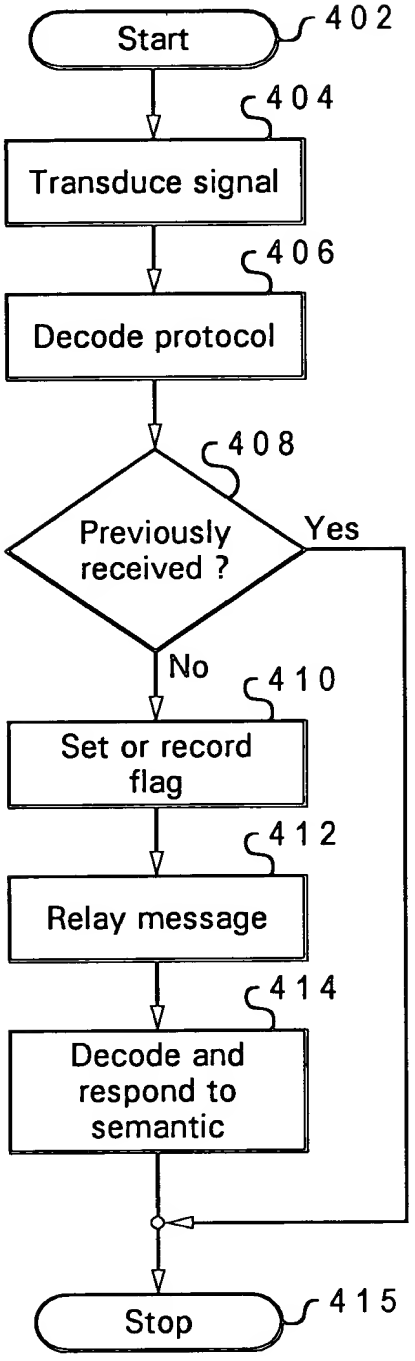
Start
402
404
Transduce signal
406
Decode protocol
408
Previously received ?
Yes
No
410
Set or record flag
412
Relay message
414
Decode and respond to semantic
Stop
415

Fig. 4A

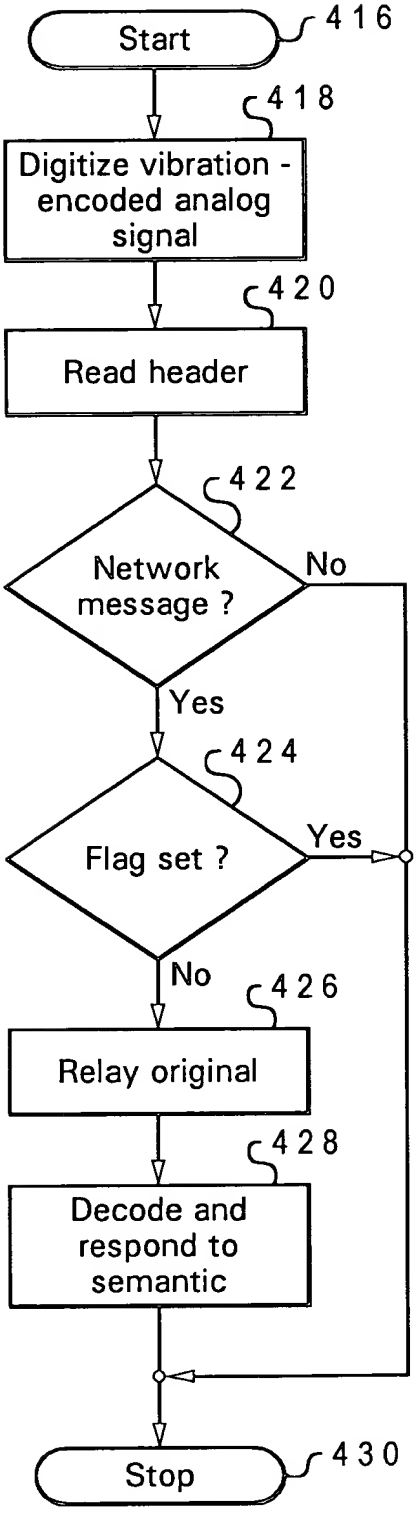
Start
416
418
Digitize vibration - encoded analog signal
420
Read header
422
Network message ?
No
Yes
424
Flag set ?
Yes
No
426
Relay original
428
Decode and respond to semantic
Stop
430

Fig. 4B

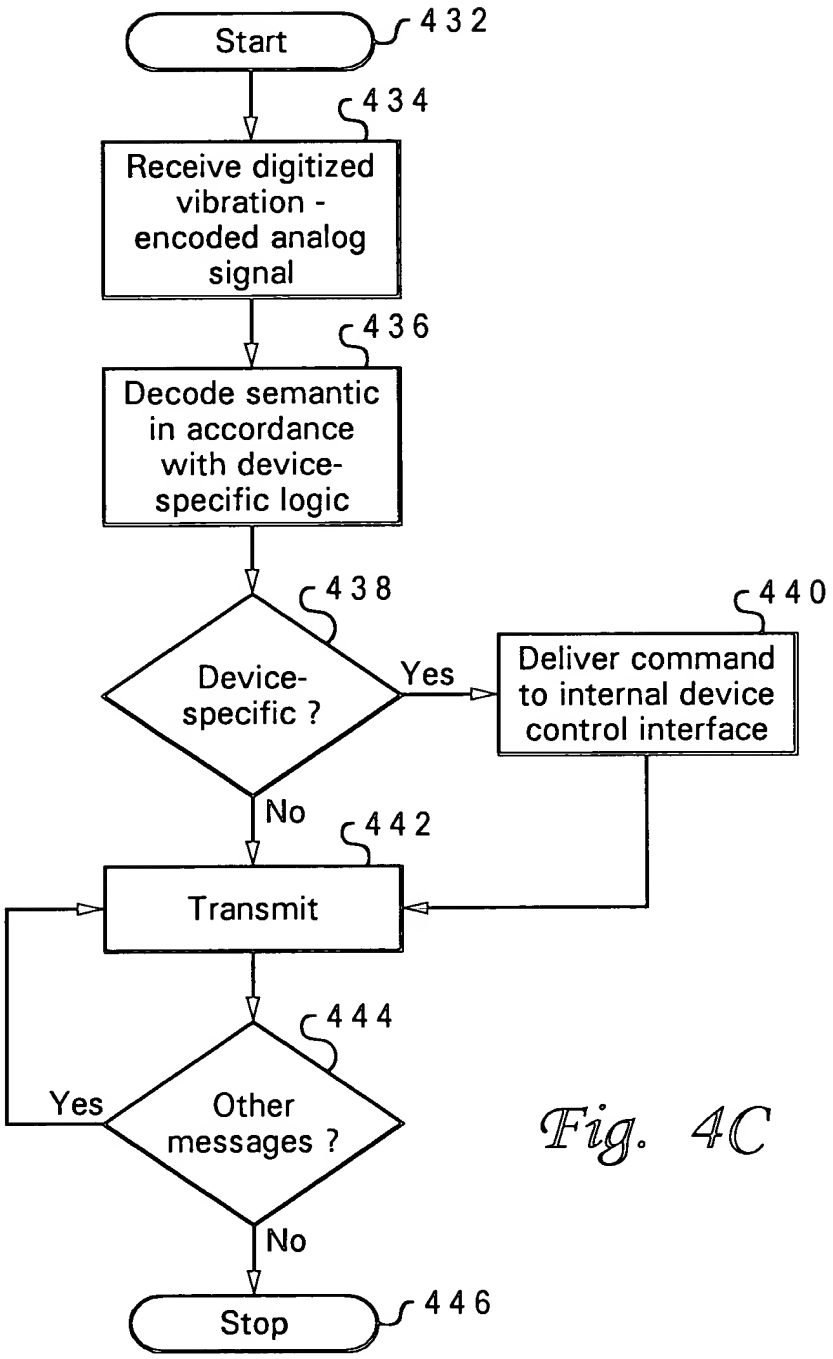
Start
432
434
Receive digitized vibration - encoded analog signal
436
Decode semantic in accordance with device-specific logic
438
Device-specific ?
Yes
440
Deliver command to internal device control interface
No
442
Transmit
444
Other messages ?
Yes
No
Stop
446

Fig. 4C